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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

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Patil, K.D.; Maiti, S.K.; Mahapatra, S.;  
[Electronics Letters](#)  
Volume 27, Issue 14, 4 July 1991 Page(s):1291 - 1293  
[AbstractPlus](#) | Full Text: [PDF](#)(172 KB) IEE JNL
- ☐ 2. **Useful path-independent integrals associated with TEM-mode transmission lines and a procedure for computation of rate of variation of capacitance with a cross-sectional dimension**  
Patil, K.D.; Maiti, S.K.; Mahapatra, S.;  
[Microwaves, Antennas and Propagation, IEE Proceedings H](#)  
Volume 140, Issue 2, April 1993 Page(s):107 - 112  
[AbstractPlus](#) | Full Text: [PDF](#)(328 KB) IEE JNL
- ☐ 3. **Design fabrication and testing of power converter and its associated circuits for a 4 kW SRM drive**  
Sengupta, M.; Maiti, S.; Sengupta, S.;  
[India Annual Conference, 2004. Proceedings of the IEEE INDICON 2004. First 20-22 Dec. 2004](#) Page(s):581 - 585  
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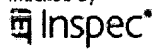
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L2	20569	"708"/\$.ccls. and @ad<"20031203"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:21
L3	256	2 and (FFT or (fast adj fourier)) and radix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:22
L4	148	3 and ((FFT or (fast adj fourier)).ti. or (FFT or (fast adj fourier)).clm. or (FFT or (fast adj fourier)).ab.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:22
L5	118	4 and butterf\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:23
L6	103	processor\$1 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:23
L7	31	distribut\$3 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:42
L8	77	radix-4 and radix-2 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:43

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L9	59	8 not 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 15:03
L10	987	708/400-409.ccls. and @ad<"20021203"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 15:04
L11	71	10 and (radix-4 and radix-2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 15:04
L12	51	11 and butterfl\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 15:05
L13	32	("4689762"   "4768159"   "4970674"   "5029079"   "5163017"   "5233551"   "5313413"   "5371696"   "5428567"   "5473556"   "5491652"   "5633817"   "5835392").PN. OR ("5991787"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/01/17 15:13
S1	65387	housing and camera	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:07

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L2	20569	"708"/\$.ccs. and @ad<"20031203"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:21
L3	256	2 and (FFT or (fast adj fourier)) and radix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:22
L4	148	3 and ((FFT or (fast adj fourier)).ti. or (FFT or (fast adj fourier)).clm. or (FFT or (fast adj fourier)).ab.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:22
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L13	32	("4689762"   "4768159"   "4970674"   "5029079"   "5163017"   "5233551"   "5313413"   "5371696"   "5428567"   "5473556"   "5491652"   "5633817"   "5835392").PN. OR ("5991787"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/01/17 15:13
S1	65387	housing and camera	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 14:07

# Inventor Information for 10/727138

Inventor Name	City	State/Country
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MAITI, SRIJIB NARAYAN	MIDNAPORE	INDIA

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10715133</a>	Not Issued	30	11/17/2003	Linearly scalable finite impulse response filter	SAHA, KAUSHIK
<a href="#">10727138</a>	Not Issued	30	12/03/2003	Linear scalable FFT/IFFT computation in a multi-processor system	SAHA, KAUSHIK
<a href="#">10781336</a>	Not Issued	30	02/17/2004	Method and system for multi-processor FFT/IFFT with minimum inter-processor data communication	SAHA, KAUSHIK
<a href="#">11003924</a>	Not Issued	30	12/03/2004	FFT/IFFT processor	SAHA, KAUSHIK
<a href="#">11191613</a>	Not Issued	30	07/28/2005	Macro-block level parallel video decoder	SAHA, KAUSHIK
<a href="#">11254069</a>	Not Issued	30	10/19/2005	System and method for clock recovery in digital video communication	SAHA, KAUSHIK

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10715133</a>	Not Issued	30	11/17/2003	Linearly scalable finite impulse response filter	MAITI, SRIJIB N.
<a href="#">10727138</a>	Not Issued	30	12/03/2003	Linear scalable FFT/IFFT computation in a multi-processor system	MAITI, SRIJIB NARAYAN
<a href="#">11003924</a>	Not Issued	30	12/03/2004	FFT/IFFT processor	MAITI, SRIJIB NARAYAN
<a href="#">11191613</a>	Not Issued	30	07/28/2005	Macro-block level parallel video decoder	MAITI, SRIJIB NARAYAN

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